FIG. 1

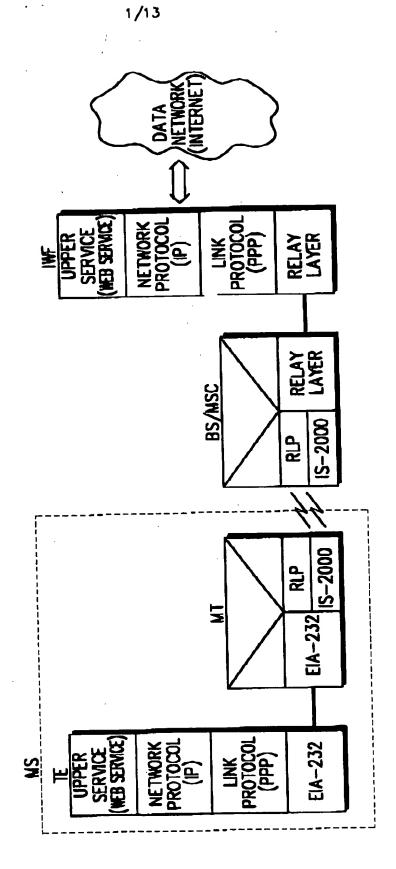
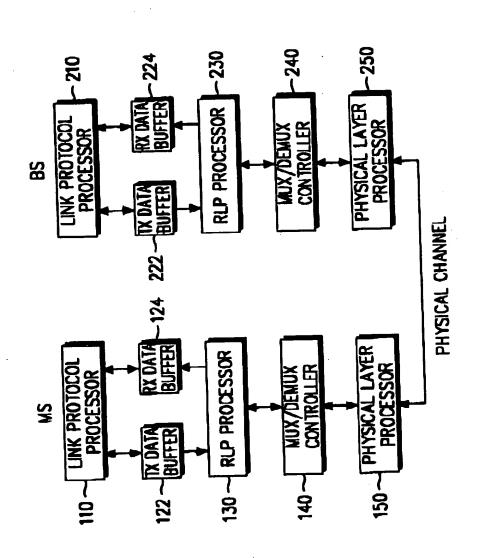


FIG. 2



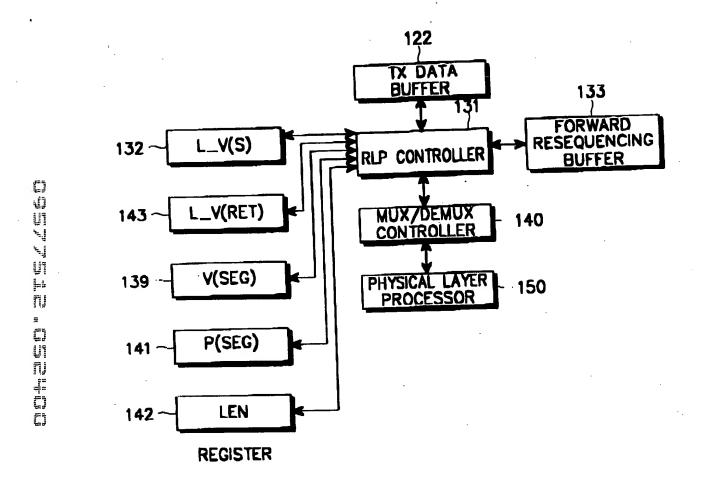


FIG. 3

35-24 15 SP (ED)

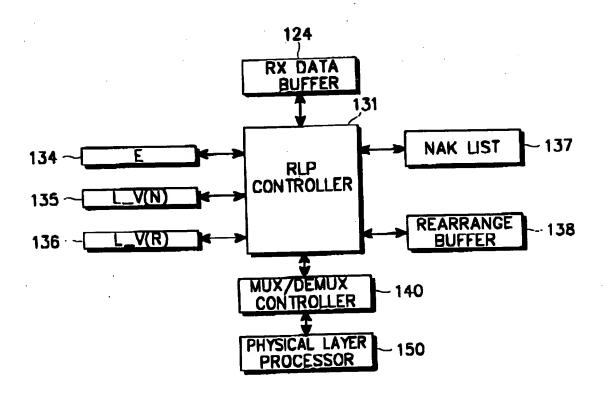
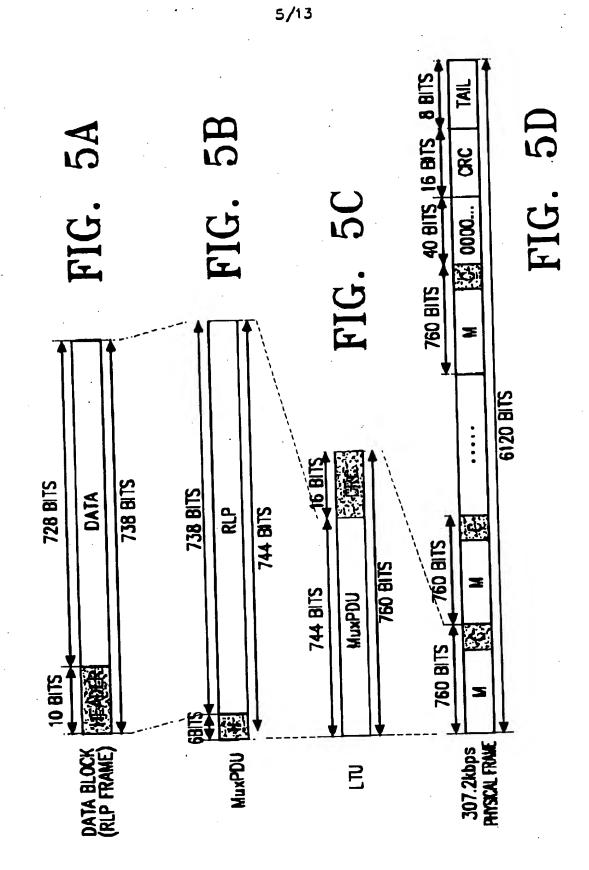


FIG. 4



.

FIG. 6A

Jay	A STINGS	C1991
760BITS	DATA BLOCK	7388175
	000 100	* 68ITS

FIG. 6B

6/13

1	CRC		16BITS	
	0 000000	200000	394BITS	
ITS	000	111 000	STORING STILL	
760BITS		DATA BLOCK	TYNDITE	STIPLES
		0010100	<b> </b>	
		10		148115
		010		7

FIG. 6C

	CAC	168175
760BITS	000000000000000000000000000000000000000	738BITS
	111 000	6BITS

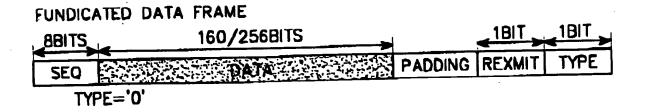
SYNC, SYNC/ACK, ACK FRAME

BBITS 16BITS 16BITS

SEQ CTL FCS PADDING TYPE

CTL'11110001' FOR SYNC,'11110010' FOR SYNC/ACK, '11110011' FOR ACK, TYPE='1'

# FIG. 7A



# FIG. 7B

#### FUNDICATED DATA FRAME

BBIT	'S_	BBITS	152/248BITS	1BIT	<b>√</b> 1BIT
SEC	3		DATA	CTL_HI	TYPE
(	בת	_HI='0',	TYPE='1'		

## FIG. 7C

### FUNDICATED DATA FRAME

BBITS	2BITS	18IT	5BITS	(LEN+1)*8BITS		₹1BIT	€1BIT
SEQ	CTL	REXMIT		DATA PA	DDING	CTL_HI	TYPE
	CTL	1='1',CTL=	'00',TY	PE='1'			

## FIG. 7D

FUNDICATED DATA FRAME

BBITS	2BITS_	1BIT	13BITS	144/240BITS		- 1BIT	<b>18</b> 1 ►
SEQ	CTL	END	SEG	DATA	PADDING	CTL_HI	TYPE
	CTL_HI=	•'1',CTL='	10',TYPE	='1'			

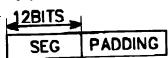
# FIG. 7E

FUNDICATED DATA FRAME

BBITS	5BITS	1BIT	5BITS	13BITS	(LEN+1)	•8BITS	1BIT	1BIT
SEQ	CTL	END	LEN	SEG	DATA	PADDING	CTL_HI	TYPE
l	. (	TL_HI=	1',CTL='11	1000',TY	<b>''</b> E='1'			

# FIG. 7F

IDLE FRAME(1/8 RATE ONLY)



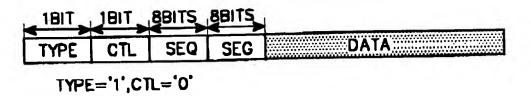
# FIG. 7G

SUPPLEMENTAL DATA FRAME

TYPE	REXMIT	SEQ	DATA
	TYPE='0'		

### FIG. 8A

SUPPLEMENTAL DATA FRAME



## FIG. 8B

SUPPLEMENTAL DATA FRAME



TYPE='1',CTL='100'

FIG. 8C

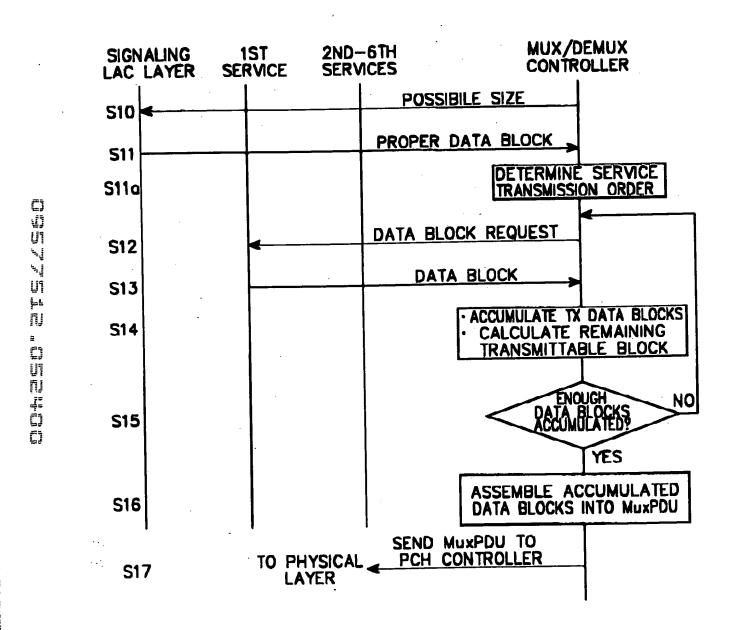


FIG. 9

5-49 NET

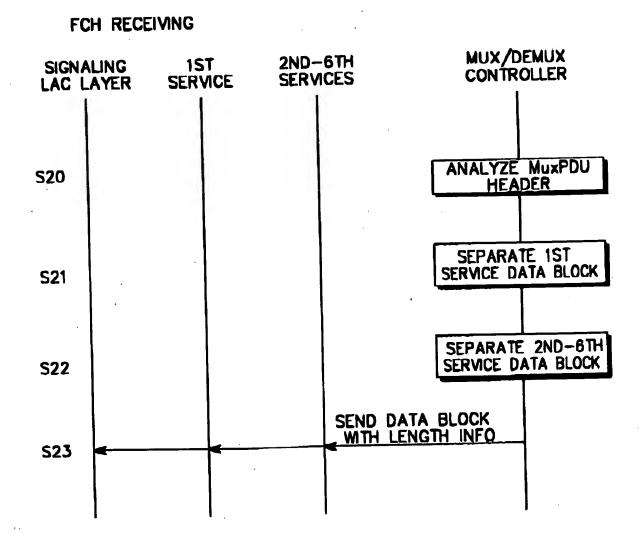


FIG. 10

FIG. 11

" (" (" (" L" (" )

#

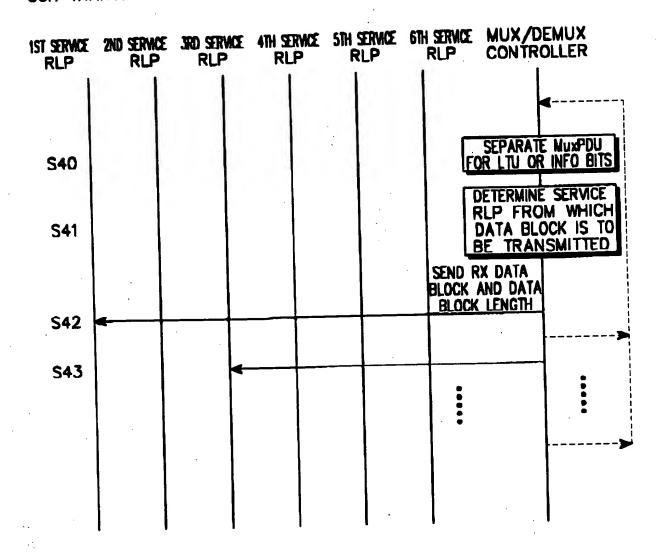


FIG. 12